



# **Global Communication Semiconductors, LLC**

Corporate & Foundry

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## **CAD Tapeout Engineer**

Job Req: #0729  
Department: CAD - COGS  
Status: Full-Time/Salaried  
Shift: Day

### **Job Description**

Designs and maintains design kits for various technologies. Design kit includes design rule manual and GDSII cell library.

- Support Foundry services
- Provide internal and external customer support in CAD tapeout related activities
- Perform layout per customer requirements
- Create WAD (Wafer-Assembly-Drawing)
- Capture and document process requirements in WAD
- Provide DRC (Design Rule Check) services and support customers on DRC feedback.
- Update design rule manual and rule deck
- Support layout services
- Works with production control team for lot start

### **Job Requirements**

- BS in Engineering. Minimum 3 years of experience in semiconductor or EDA industry
- Strong knowledge in layout software. Virtuoso, ADS, or dw-2000
- Masks procurement. PO requisition. Schedule, track, and document orders
- Proficient in MS Office. Captures and documents process requirements
- Understand design rules. Provide DRC services. Update DRC/LVS (Layout vs Schematic) decks
- Knowledge of UNIX or Linux
- Proficient in typing
- Ability to handle time sensitive assignments
- Ability to multi-task.